What is claimed is:

1. A method comprising:

generating a processor instruction proxy stub associated with one or more processor instructions; and

generating an optimized managed application program interface based on the processor instruction proxy stub to enable one or more features of a processor associated with the one or more processor instructions.

2. A method as defined in claim 1, wherein generating the processor instruction proxy stub associated with the one or more processor instructions comprises generating the processor instruction proxy stub at a layer associated with a virtual machine of a managed runtime environment.

3. A method as defined in claim 1, wherein generating the processor instruction proxy stub associated with the one or more processor instructions comprises generating the processor instruction proxy stub during installation of a managed runtime application.

4. A method as defined in claim 1, wherein generating the processor instruction proxy stub associated with the one or more processor instructions comprises generating the processor instruction proxy stub associated with one of a Streaming SIMD Extension (SSE) instruction, an SSE2 instruction, and a MultiMedia Extension instruction.

5. A method as defined in claim 1, wherein generating the processor instruction proxy stub associated with the one or more processor instructions comprises generating the processor instruction proxy stub via marshaling language code of a virtual machine.

6. A method as defined in claim 1, wherein generating the processor instruction proxy stub associated with the one or more processor instructions of the processor comprises generating the processor instruction proxy stub in response to identifying the processor associated with the one or more processor instructions.

7. A method as defined in claim 1 further comprising enabling a feature associated with the one or more processor instructions during execution of a managed runtime application based on the optimized managed application program interface.

8. A machine accessible medium having instructions, which when executed, cause a machine to:

generate a processor instruction proxy stub associated with one or more processor instructions; and

generate an optimized managed application program interface based on the processor instruction proxy stub to enable one or more features of a processor associated with the one or more processor instructions.

9. A machine accessible medium as defined in claim 8, wherein the instructions cause the machine to generate the processor instruction proxy stub associated

with the one or more processor instruction by generating the processor instruction proxy stub at a layer associated with a virtual machine of a managed runtime environment.

10. A machine accessible medium as defined in claim 8, wherein the instructions cause the machine to generate the processor instruction proxy stub associated with the one or more processor instructions comprises generating the processor instruction proxy stub during installation of a managed runtime application.

11. A machine accessible medium as defined in claim 8, wherein the instructions cause the machine to generate the processor instruction proxy stub associated with the one or more processor instructions comprises generating the processor instruction proxy stub associated with one of a Streaming SIMD Extension (SSE) instruction, an SSE2 instruction, and a MultiMedia Extension instruction.

12. A machine accessible medium as defined in claim 8, wherein the instructions cause the machine to generate the processor instruction proxy stub associated with one or more processor instructions by generating the processor instruction proxy stub via marshaling language code of a virtual machine.

13. A machine accessible medium as defined in claim 8, wherein the instructions cause the machine to generate the processor instruction proxy stub associated with the one or more processor instructions of the processor by generating the processor instruction proxy stub in response to identifying the processor associated with the one or more processor instructions.

14. A machine accessible medium as defined in claim 8, wherein the instructions cause the machine to enable a feature associated with the one or more processor instructions for execution of a managed runtime application based on the optimized managed application program interface.

15. A machine accessible medium as defined in claim 8, wherein the machine accessible medium comprises one of a programmable gate array, application specific integrated circuit, erasable programmable read only memory, read only memory, random access memory, magnetic media, and optical media.

16. An apparatus comprising:

a processor instruction proxy stub generator to generate a processor instruction proxy stub associated with one or more processor instructions and to generate an optimized managed application program interface based on the processor instruction proxy stub; and

a compiler to compile the optimized managed application program interface to enable one or more features of a processor associated with the one or more processor instructions.

17. An apparatus as defined in claim 16, wherein the processor instruction proxy stub generator is integrated into one of a virtual machine and the compiler.

18. An apparatus as defined in claim 16, wherein the processor instruction proxy stub generator identifies a processor associated with the one or more processor instructions to generate the processor instruction proxy stub.

19. An apparatus as defined in claim 16, wherein one or more processor instructions comprise one of a Streaming SIMD Extension (SSE) instruction, an SSE2 instruction, and a MultiMedia Extension instruction instruction.

20. An apparatus as defined in claim 16, wherein the compiler comprises a just-in-time compiler.

21. An apparatus as defined in claim 16, wherein the processor instruction proxy stub is generated at a layer associated with a virtual machine of a managed runtime environment.

22. An apparatus as defined in claim 16, wherein the optimized managed application interface program enables a feature associated with the one or more processor instructions for execution of a managed runtime application.

23. A processor system comprising:

a dynamic random memory (DRAM) to store one or more optimized managed application program interfaces; and

a processor coupled to the DRAM to generate a processor instruction proxy stub associated with one or more processor instructions, and to generate an optimized managed application program interface based on the processor instruction proxy stub to enable one or more features of the processor associated with the one or more processor instructions.

24. A processor system as defined in claim 23, wherein one or more processor instructions comprise one of a Streaming SIMD Extension (SSE) instruction, an SSE2 instruction, and a MultiMedia Extension instruction instruction.

25. A processor system as defined in claim 23, wherein the processor instruction proxy stub is generated at a layer associated with a virtual machine of a managed runtime environment.

26. A processor system as defined in claim 23, wherein the processor instruction proxy stub is generated during installation of a managed runtime application.

27. A processor system as defined in claim 23, wherein the optimized managed application interface program enables a feature associated with the one or more processor instructions during execution of a managed runtime application.